

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An in-plane switching mode liquid crystal display device, comprising:
- a plurality of gate lines and data lines defining a plurality of pixels;
 - a thin film transistor in each of the pixels, the thin film transistor including a gate electrode on a substrate, an insulating layer over the gate electrode, a semiconductor layer on the insulating layer, a source electrode and a drain electrode on the semiconductor layer;
 - a common line on the substrate;
 - at least one pixel electrode having a predetermined width in each of the pixels; and
 - a common electrode in each of the pixels, the common electrode being substantially parallel to the pixel electrode, the common electrode including two first portions disposed along the data line to cover complete the data line and only one second portion disposed between the first portions, thereby each of the pixels including two areas defined by the first portion and the second portion;
 - a passivation layer over the source electrode, drain electrode and semiconductor layer, the passivation layer being made of an organic material including at least one material of BCB (Benzo-Cyclo-Butene) and photoacryl; and
 - wherein the pixel electrode and the common electrode are disposed on the same layer, the common electrode and the common line are disposed on layers different from each other so that the common electrode is connected to the common line through a contact hole formed in the insulating layer and the passivation layer, the pixel electrode and the common electrode being disposed on the passivation layer,
 - wherein the common electrode and the common line are not overlapped with the pixel electrode and the common line is separated a predetermined distance from the end portion of the pixel electrode.

2-3. (Cancelled)

4. (Previously Presented) The device of claim 1, wherein the data lines are formed on the insulating layer.

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5-16. (Cancelled)